

*cont.*  
recognizing means being coupled to an input of said element controller to receive a grant message as described above, and said recognizing means is further included to recognize in said grant message said first plurality of bits and said second plurality of bits and to generate upon said recognition a control signal for a selected element in order to thereby impose execution of said locally predefined function upon said selected element.

Finally, the invention is directed to a tree-like network including either or both of a line terminator and an element controller as described above.--

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Page 4, lines 19-20, ~~delete~~ "This is described in claim 2."

Page 5, lines 23-<sup>24</sup>~~34~~, ~~delete~~ "This is described in the method of claim 3 that is realized by the line terminator of claim 5 and by the element controller of claim 6.--

Page 6, lines 14-15, ~~delete~~ "The element controller associated to such a switchable amplifier is described in claim 7";

before line 20, ~~insert~~ the heading: --**Brief Description of the Drawings**--.

after line 23, ~~insert~~ the heading: --**Detailed Description of the Invention**--.

**IN THE CLAIMS:**

Claim 3, line 2, ~~delete~~ "any one of claim 1 and claim 2".

Claim 9, line 3, ~~delete~~ "according to any one of claim 4 and claim 5" and ~~delete~~ "any one of claim 6, claim 7, and claim 8" and ~~insert~~ --claim 6--.

**IN THE ABSTRACT:**

After the heading, ~~delete~~ the title in its entirety.